

REMARKS

Prior to this Reply, Claims 1-51 were pending. Through this Reply, Claims 1-51 have been cancelled without prejudice to, or disclaimer of, the subject matter contained therein. In addition, Claims 52-151 have been added. Accordingly, Claims 52-151 are now at issue in the present case.

I. Claim Rejections

The Examiner rejected Claims 12, 19, 30 and 43 under 35 U.S.C. § 112, first paragraph, as failing to comply with the enablement requirement.

The Examiner also rejected Claims 14, 16 and 17 under 35 U.S.C. § 112, second paragraph, as failing to have sufficient antecedent basis.

The Examiner also rejected Claims 19 and 43 under 35 U.S.C. § 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

The Examiner also rejected Claims 1, 2, 7, 9-11, 14, 16, 18-21, 24, 26-29, 32, 34, 36, 37, 40-44, 46, 47, 49 and 51 under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent No. 5,727,184 to Richter et al. (hereinafter "Richter et al.").

The Examiner also rejected Claims 3-6, 22-23, 38, 39 and 45 under 35 U.S.C. § 103(a) as being unpatentable over Richter et al. in view of U.S. Patent No. 5,504,868 to Krakirian.

The Examiner also rejected Claims 8 and 25 under 35 U.S.C. § 103(a) as being unpatentable over Richter et al. in view of U.S. Patent No. 4,926,314 to Dhuey.

The Examiner also rejected Claims 12, 13, 30, 31, 48 and 50 under 35 U.S.C. § 103(a) as being unpatentable over Richter et al.

The Examiner also rejected Claims 15 and 33 under 35 U.S.C. § 103(a) as being unpatentable over Richter et al. in view of U.S. Patent No. 6,223,229 to Kvamme.

In response, in order to expedite allowance of the present application, Applicant has cancelled Claims 1-51 without prejudice to, or disclaimer of, the subject matter contained therein.

II. New Claims

Claims 52-151 have been added. No new matter has been added.

Claim 52 recites “receiving a selection command that includes a selection identifier from the data lines [of a single ATA bus]” and “asserting PDIAG and INTRQ [of the single ATA bus] in response to the selection identifier matching the assigned identifier.” Richter et al., alone or in combination with the other references of record, fails to teach or suggest this approach, as the Examiner correctly notes in objecting to Claim 35. Claims 53-61 depend from Claim 52 and are believed to be allowable for at least the same reasons as Claim 52.

Claim 62 is believed to be allowable for at least the same reasons as Claim 52, and Claims 63-71 depend from Claim 62 and are believed to be allowable for at least the same reasons as Claim 62.

Claim 72 is believed to be allowable for at least the same reasons as Claim 52, and Claims 73-81 depend from Claim 72 and are believed to be allowable for at least the same reasons as Claim 72.

Claim 82 is believed to be allowable for at least the same reasons as Claim 52, and Claims 83-91 depend from Claim 82 and are believed to be allowable for at least the same reasons as Claim 82.

Claim 92 is believed to be allowable for at least the same reasons as Claim 52, and Claims 93-101 depend from Claim 92 and are believed to be allowable for at least the same reasons as Claim 92.

Claim 102 is believed to be allowable for at least the same reasons as Claim 52, and Claims 103-111 depend from Claim 102 and are believed to be allowable for at least the same reasons as Claim 102.

Claim 122 recites “the controller includes a selected status register, the ATA bus . . . includes . . . first and second DASP lines . . . connected to the selected status register, the first device is connected to . . . the first DASP line . . . the second device is connected to . . . the second DASP line” and “the first device asserting DASP in response to the selection identifier matching the first assigned identifier, thereby setting a first bit in the selected status register” and “the second device negating DASP in response to the selection identifier not matching the second assigned identifier, thereby not setting a second bit in the selected status register” and “the controller sending a read/write command across the ATA bus to the first device in response to reading the selected status register to verify that the first device is selected and the second device is not selected.” Richter et al., alone or in combination with the other prior art of record, fails to teach or suggest this approach. Instead, cable 110 is an ATA standard 40 signal ribbon cable (col. 7, lines 50-51). Thus, cable 110 does not include multiple DASP lines, and system adapter 204 does not include a selected status register with separate bits corresponding to DASP assertion and negation by disk drives 114 and 115. Claims 123-131 depend from Claim 122 and are believed to be allowable for at least the same reasons as Claim 122.

Claim 132 is believed to be allowable for at least the same reasons as Claim 122, and Claims 133-141 depend from Claim 132 and are believed to be allowable for at least the same reasons as Claim 132.

Claim 142 is believed to be allowable for at least the same reasons as Claim 122, and Claims 143-151 depend from Claim 142 and are believed to be allowable for at least the same reasons as Claim 142.

III. Amendments to Specification

A substitute specification without claims (and a marked-up version thereof) is provided herein under 37 C.F.R. 1.125 to improve clarity of the specification. No new matter has been added.

Applicants respectfully request that the substitute specification be entered.

IV. Amendments to Drawings

Applicants are submitting replacement Figures 1-7 (contained on Replacement Sheets 1-7) to improve the quality of the drawings.

Figure 1 has been modified to clarify computer system 100, host computer 104, bus system 106, controller 120 and devices 128 as 128-0, 128-1, 128-2, 128-3, 128-4, 128-5, 128-6 and 128-7.

Figure 2 has been modified at step 204 to change “device” to “devices” and “bus drivers” to “lines” and at step 216 to change “negates” to “clears” and “, asserts” to “and sets” and to delete “in status register of drive” and at step 220 to change “negated” to “cleared” and “asserted” to “bit set” and at step 224 to change “power up” to “power-up.”

Figure 3 has been modified at step 300 to change “received” to “with selection identifier sent” and “bus” to “lines” and to delete “device” and “in” and at step 304 to insert “selection” before the first instance of “identifier” and to delete “of selected device” and “the” and at step 308 to insert “additional” before “hardware” and to delete “for use” and “registers” and at step 312 to change “bus” to “lines drivers” and at step 332 to change “bus” to “lines” and at step 340 to insert “additional” before “hardware” and to delete “for use” and “registers” and at step 344 to change “bus drivers” to “lines” and at step 352 to change “bus drivers” to “lines.”

Figure 4 has been modified at step 400 to insert “computer” before “issues” and to delete the first instance of “device” and at step 404 to change “bus” to “lines” and to delete “device” and at step 408 to change “bus drivers, INTRQ,” to “lines and INTRQ and” and at step 420 to change “,” to “and” and “bus” to “lines” and at step 432 to insert “computer” before “reads” and “selected” before the first instance of “device” and to delete “of device” and at step 436 to insert “computer” before “issues” and change “and/or sends” to “to send/receive.”

Figure 5 has been modified to clarify control registers 500, selected status register 516, ready status register 520, interrupt pending register 524 and interrupt mask register 528, to reshape AND gate 504 and OR gate 508, to insert “computer” after “host” at interrupt request signal line 512, to change all instances of “IRQ” to “INTRQ” and all instances of “PIADG” to “PDIAG” and to delete all instances of “DRV.”

Figure 6 has been modified at step 612 to change “negated” to “negate” and at step 620 to insert “computer” before “may” and to delete “the.”

Figure 7 has been modified at step 700 to change “drive” to “device” and to insert “computer” before “issues” and to delete “device” and at step 704 to change “bus” to “lines” and to delete “device” and at step 708 to change “bus drivers,” to “lines and” and at step 712 to

change “bus” to “lines” and “,” to “and” and at step 720 to change “a second” to “another” and to insert “computer” before “issues.”

No new matter has been added. Figures 1-7 constitute all of the drawings of the application.

V. Additional Claim Fees

In determining whether additional claim fees are due, reference is made to the Fee Calculation Table (below).

Fee Calculation Table						
	Claims Remaining After Amendment		Highest Number Previously Paid For	Present Extra	Rate	Additional Fee
Total (37 CFR 1.16(c))	100	Minus	51	= 49	x \$50 =	\$2450.00
Independent (37 CFR 1.16(b))	10	Minus	5	= 5	x \$200 =	\$1000.00

As set forth in the Fee Calculation Table (above), Applicants previously paid claim fees for fifty-one (51) total claims and for five (5) independent claims. Therefore, Applicants hereby authorize the Commissioner to charge the credit card identified on the enclosed Form PTO-2038 in the amount of \$3450.00 for the presentation of forty-nine (49) total claims over fifty-one (51) and five (5) independent claims over five (5). Although Applicants believe that no other fees are due, the Commissioner is hereby authorized to charge Deposit Account No. 50-2198 for any fee deficiencies associated with filing this paper.

VI. Conclusion

It is believed that the above comments establish patentability. Applicants do not necessarily accede to the assertions and statements in the Office Action, whether or not expressly addressed.

Applicants believe that the application appears to be in form for allowance. Accordingly, reconsideration and allowance thereof is respectfully requested.

The Examiner is invited to contact the undersigned at the below-listed telephone number regarding any matters relating to the present application.

Respectfully submitted,

A handwritten signature in cursive script, appearing to read "Tejpal S. Hansra", written over a horizontal line.

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